INFORMATION DISCLOSURE STATEMENT BY APPLICANT PTO FORM 1449

Atty. Docket No.

02885/94

Applicant(s)

MARTIN VORBACH

International Filing Date
8 September 2003

Serial No.
10/526,595

Group Art Unit
Not Yet Assigned

7		Not Yet Assigned				
APR 2 1 2005		U. S. PATENT DOCU				
EXAMINER'S INITIAL SEPTEMBER 1	PATENT/ PUBLICATION NUMBER	PATENT/ PUBLICATION DATE	NAME	CLAS S	SUB CLASS	FILING DATE
m	RE34363	August 31, 1993	Freeman	1		
i	2,067,477	January 12, 1937	J.B. Cooper			
	3,242,998	March 29, 1966	C.H. Gubbins			
	3,681,578	August 1, 1972	Stevens	 		
	3,757,608	September 11, 1973	Willner	1		
	3,855,577	December 17, 1974	Vandierendonck	1		
	4,489,857	February 6, 1996	Agrawal et al.			
	4,498,134	February 5, 1985	Etchells et al.			
	4,498,172	February 5, 1985	Bhavsar			
	4,566,102	January 21, 1986	Hefner		<u> </u>	
	4,591,979	May 27, 1986	Iwashita			
	4,663,706	May 5, 1987	James et al.			
,	4,682,284	July 21, 1987	Schrofer			
	4,706,216	November 10, 1987	Carter			
	4,720,780	January 19, 1988	Dolecek			
	4,739,474	April 19, 1988	Holsztynski			1
	4,761,755	August 2, 1988	Ardini et al.			<u> </u>
	4,811,214	March 7, 1989	Nosenchuck et al.			
	4,852,043	July 25, 1989	Guest			
	4,852,048	July 25, 1989	Morton			<u> </u>
	4,860,201	August 22, 1989	Miranker et al.	 		<u> </u>
	4,870,302	September 26, 1989	Freeman			<u> </u>
	4,891,810	January 2, 1990	de Corlieu et al.	1		
	4,901,268	February 13, 1990	Judd			1
	4,910,665	March 20, 1990	Mattheyses et al.			
	4,967,340	October 30, 1990	Dawes			
	5,014,193	May 7, 1991	Garner et al.			
	5,015,884	May 14, 1991	Agrawal et al.			
	5,021,947	June 4, 1991	Campbell et al.			
	5,023,775	June 11, 1991	Poret		1	
	5,043,978	August 27, 1991	Nagler et al.			
	5,047,924	September 10, 1991	Matsubara et al.	-		<u> </u>
	5,065,308	November 12, 1999	Evans			<u> </u>
	5,072,178	December 10, 1991	Matsumoto			
m	5,081,375	January 14, 1992	Pickett et al.	<u> </u>		

FILING DATE	SUB CLASS	i	CLAS S	NAME	PATENT/ PUBLICATION DATE	PATENT/ PUBLICATION NUMBER	EXAMINER'S INITIALS
		_	1	Cruickshank et al.	April 28, 1992	5,109,503	AL
	,	_		Evan et al.	May 12, 1992	5,113,498	1
·		_		Okamoto et al.	May 19, 1992	5,115,510	
				Hillis	June 16, 1992	5,123,109	
				Nabity et al.	June 30, 1992	5,125,801	
			 	Steele	July 7, 1992	5,128,559	
			1	Weisenborn	August 25, 1992	5,142,469	
	<u> </u>			Camarota et al.	September 1, 1992	5,144,166	
				Lee et al.	March 9, 1993	5,193,202	
		_	1	Horst	April 13, 1993	5,203,005	
				Mihara et al.	April 20, 1993	5,204,935	
			1	Ebeling et al	May 4, 1993	5,208,491	
		\top	 	Thayer et al.	July 6, 1993	5,226,122	
		++	 	Agrawal et al.	August 3, 1993	5,233,539	
		\forall	1	Ewert	September 21, 1993	5,247,689	•
		-\-		Proebsting	December 28, 1993	5,274,593	
		-		Horst	February 15, 1994	5,287,472	
		-+		Vincent et al.	March 15, 1994	5,294,119	
		+		Estes et al.	April 5, 1994	5,301,284	
		+		Kolchinsky	April 5, 1994	5,301,344	
				Magar et al.	April 12, 1994	5,303,172	
				Popli et al.	August 9, 1994	5,336,950	
		++		Rechtschaffen et al.	September 13, 1994	5,347,639	
				Mott et al.	September 20, 1994	5,349,193	
		 - 		Richek et al.	October 4, 1994	5,353,432	
				Gilson	November 1, 1994	5,361,373	
		\dashv		Mumme	January 3, 1995	5,379,444	
		+		Schmidt et al.	April 25, 1995	5,410,723	
				Morley et al.	May 23, 1995	5,418,952	
		+		Holsztynski et al.	May 30, 1995	5,421,019	
				Agrawal et al.	June 6, 1995	5,422,823	
		+		Liu et al.	June 13, 1995	5,425,036	-
-		+		Ong	June 20, 1995	5,426,378	.
		+		Flood et al.	June 27, 1995	5,428,526	
		\vdash		Hung et al.	July 4, 1995	5,430,687	
	1	+	-	Galbraith et al.	August 8, 1995	5,440,245	
	 	+		Olsen et al.	August 15, 1995	5,440,538	
	 	+-		Nosenchuck	August 15, 1995	5,442,790	
				Watson et al.	August 22, 1995	5,444,394	
		_		Kawata	September 5, 1995	5,448,186	W,

EXAMINER'S INITIALS	PATENT/ PUBLICATION NUMBER	PATENT/ PUBLICATION DATE	NAME	CLAS S	SUB CLASS	FILING DATE
al	5,455,525	October 3, 1995	Ho et al.			
P	5,457,644	October 10, 1995	McCollum			
	5,465,375	November 7, 1995	Thepaut et al.			
	5,473,266	December 5, 1995	Ahanin et al.			 -
	5,473,267	December 5, 1995	Stansfield			
	5,475,583	December 12, 1995	Bock et al.			
	5,475,803	December 12, 1995	Stearns et al.			
	5,475,856	December 12, 1995	Kogge			
	5,483,620	January 9, 1996	Pechanek et al.			
	5,485,103	January 16, 1996	Pedersen et al.			
	5,485,104	January 16, 1996	Agrawal et al.			1
	5,489,857	February 6, 1996	Agrawal et al.	-		
	5,491,353	February 13, 1996	Kean	 		
	5,493,239	February 20, 1996	Zlotnick			<u> </u>
	5,497,498	March 5, 1996	Taylor	 \ 		
	5,506,998	April 9, 1996	Kato et al.	 		1
	5,510,730	April 23, 1996	El Gamal et al.	 	-	
	5,511,173	April 23, 1996	Yamaura et al.	\		
	5,513,366	April 30, 1996	Agarwal et al.			
	5,521,837	May 28, 1996	Frankle et al.		 	
	5,522,083	May 28, 1996	Gove et al.	,	\	ļ
	5,530,873	June 25, 1996	Takano		+	-
	5,530,946	June 25, 1996	Bouvier et al.	<u> </u>	-	
	5,532,693	July 2, 1996	Winters et al.		1	
	5,532,957	July 2, 1996	Malhi			
	5,535,406	July 9, 1996	Kolchinsky			
	5,537,057	July 16, 1996	Leong et al.			<u> </u>
	5,537,601	July 16, 1996	Kimura et al.			-
	5,541,530	July 30, 1996	Cliff et al.			1
	5,544,336	August 6, 1996	Kato et al.			
	5,548,773	August 20, 1996	Kemeny et al.			
	5,555,434	September 10, 1996	Carlstedt			
1	5,559,450	September 24, 1996	Ngai et al.			
	5,561,738	October 1, 1996	Kinerk et al.	<u> </u>		
	5,570,040	October 29, 1996	Lytle et al.			
	5,574,930	November 12, 1996	Halverson Jr. et al.			
	5,583,450	December 10, 1996	Trimberger et al.			
	5,586,044	December 17, 1996	Agrawal et al.		-\-	+ -
	5,587,921	December 24, 1996	Agrawal et al.		+	
W	5,588,152	December 24, 1996	Dapp et al.		 	

EXAMINER'S INITIALS	PATENT/ PUBLICATION NUMBER	PATENT/ PUBLICATION DATE	NAME	CLAS S	SUB CLASS	FILING DATE
he	5,590,345	December 31, 1996	Barker et al.			
. 1	5,590,348	December 31, 1996	Phillips et al.			<u> </u>
	5,596,742	January 21, 1997	Agarwal et al.			
	5,600,265	February 4, 1997	El Gamal Abbas et al.			
	5,600,845	February 4, 1997	Gilson			
	5,611,049	March 11, 1997	Pitts			
	5,617,547	April 1, 1997	Feeney et al.			
	5,625,806	April 29, 1997	Kromer			
	5,634,131	May 27, 1997	Matter et al.			
	5,649,176	July 15, 1997	Selvidge et al.			
	5,649,179	July 15, 1997	Steenstra et al.	1		
	5,652,894	July 29, 1997	Hu et al.			
	5,655,069	August 5, 1997	Ogawara et al.			
	5,655,124	August 5, 1997	Lin			
	5,657,330	August 12, 1997	Matsumoto			
	5,658,797	August 19, 1997	Zandveld et al.			
<u> </u>	5,675,743	October 7, 1997	Mavity		1	<u> </u>
	5,680,583	October 21, 1997	Kuijsten			
	5,713,037	January 27, 1998	Wilkinson et al.			1
	5,717,943	February 10, 1998	Barker et al.			
-	5,732,209	March 24, 1998	Vigil et al.		 	
	5,734,921	March 31, 1998	Dapp et al.	-	\	
	5,742,180	April 21, 1998	Detton et al.	<u></u>		
	5,748,872	May 5, 1998	Norman			
	5,754,827	May 19, 1998	Barbier et al.			1
	5,754,871	May 19, 1998	Wilkinson et al.			
	5,760,602	June 2, 1998	Tan			
	5,761,484	June 2, 1998	Agarwal et al.	 		
	5,773,994	June 30, 1998	Jones	-		
	5,778,439	July 7, 1998	Timberger et al.			
	5,784,636	July 21, 1998	Rupp			
	5,794,059	August 11, 1998	Barker et al.			
	5,794,062	August 11, 1998	Baxter			
	5,801,715	September 1, 1998	Norman			
	5,802,290	September 1, 1998	Casselman			
	5,828,229	October 27, 1998	Cliff et al.			
	5,828,858	October 27, 1998	Athanas et al.			
	5,838,165	November 17, 1998	Chatter			<u> </u>
	5,844,888	December 1, 1998	Narjjyka			1
12	5,848,238	December 8, 1998	Shimomura et al.	<u> </u>		

EXAMINER'S INITIALS	PATENT/ PUBLICATION NUMBER	PATENT/ PUBLICATION DATE	NAME	CLAS S	SUB CLASS	FILING DATE
100	5,854,918	December 29, 1998	Baxter	1		
	5,859,544	January 12, 1999	Norman			
	5,865,239	February 2, 1999	Сатт			
	5,867,691	February 2, 1999	Shiraishi			1
	5,867,723	February 2, 1999	Peters et al.			
	5,884,075	March 16, 1999	Hester et al.			
	5,887,162	March 23, 1999	Williams et al.			
	5,887,165	March 23, 1999	Martel et al.			
	5,889,982	March 30, 1999	Rodgers et al.			
	5,892,370	April 6, 1999	Eaton et al.			
	5,892,961	April 6, 1999	Trimberger			
	5,901,279	May 4, 1999	Davis III			<u></u>
	5,915,123	June 22, 1999	Mirsky et al.	-		
	5,924,119	July 13, 1999	Sindhu et al.			<u> </u>
	5,927,423	July 27, 1999	Wada et al.			
	5,933,642	August 3, 1999	Baxter et al.			
	5,936,424	April 10, 1999	Young et al.			ļ
	5,943,242	August 24, 1999	Vorbach et al.			
	5,956,518	September 21, 1999	DeHon et al.			
·	5,966,534	October 12, 1999	Cooke et al.	 		
	5,970,254	October 19, 1999	Cooke et al.		\	-
	5,978,260	November 2, 1999	Trimberger et al.		}	
	6,011,407	January 4, 2000	New		 	<u> </u>
	6,014,509	January 11, 2000	Furtek et al.		+	
	6,020,758	February 1, 2000	Patel et al.			
	6,021,490	February 1, 2000	Vorbach et al.		 	
	6,023,564	February 8, 2000	Trimberger		 	
	6,023,742	February 8, 2000	Ebeling et al.		 	<u> </u>
	6,034,538	March 7, 2000	Abramovici		\	<u> </u>
	6,038,650	March 14, 2000	Vorbach et al.		 	
	6,038,656	March 14, 2000	Cummings et al.		 	
	6,047,115	April 4, 2000	Mohan et al.	-	 	
	6,049,222	April 11, 2000	Lawman		 	+
	6,052,773	April 18, 2000	DeHon et al.		 	
	6,054,873	April 25, 2000	Laramie		 	-
	6,058,469	May 2, 2000	Baxter		 	+
	6,081,903	June 27, 2000	Vorbach et al.		 	
	6,085,317	July 4, 2000	Smith			
	6,086,628					1
		July 11, 2000	Dave et al.	ļ.,		1
w	6,088,795	July 11, 2000	Vorbach et al.	/		

EXAMINER'S INITIALS	PATENT/ PUBLICATION NUMBER	PATENT/ PUBLICATION DATE	NAME	CLAS S	SUB CLASS	FILING DATE
42	6,092,174	July 18, 2000	Roussakov			
	6,105,105	August 15, 2000	Trimberger et al.			
	6,108,760	August 22, 2000	Mirsky et al.	-		
	6,119,181	September 12, 2000	Vorbach et al.			
	6,122,719	September 19, 2000	Mirsky et al.			
	6,125,408	September 26, 2000	McGee et al.			
	6,127,908	October 3, 2000	Bozler et al.		<u>.</u>	
	6,150,837	November 21, 2000	Beal et al.			
	6,150,839	November 21, 2000	New et al.			
	6,172,520	January 9, 2001	Lawman et al.			
	6,173,434	January 9, 2001	Wirthlin et al.			
	6,202,182	March 13, 2001	Abramovici et al.			
	6,219,833	April 17, 2001	Solomon et al.			
	6,230,307	May 8, 2001	Davis et al.	1		
	6,240,502	May 29, 2001	Panwar et al.			
	6,243,808	June 5, 2001	Wang			
	6,260,179	July 10, 2001	Ohsawa et al.			<u> </u>
	6,263,430	July 17, 2001	Trimberger et al.			
	6,279,077	August 21, 2001	Nasserbakht et al.	1		
	6,282,627	August 28, 2001	Wong et al.		\	
	6,288,566	September 11, 2001	Hanrahan et al.		<u> </u>	
	6,289,440	September 11, 2001	Casselman			
	6,298,472	October 2, 2001	Phillips et al.			
	6,311,200	October 30, 2001	Hanrahan et al.			
	6,321,366	November 20, 2001	Tseng et al.			
	6,321,373	November 20, 2001	Ekanadham et al.			
	6,338,106	January 8, 2002	Vorbach et al.		1	
	6,341,318	January 22, 2002	Dakhil			
	6,347,346	February 12, 2002	Taylor			
	6,349,346	February 19, 2002	Hanrahan et al.			
	6,370,596	April 9, 2002	Dakhil			
	6,378,068	April 23, 2002	Foster et al.			
	6,389,379	May 14, 2002	Lin et al.			
	6,389,579	May 14, 2002	Phillips et al.			
	6,392,912	May 21, 2002	Hanrahan et al.			
	6,404,224	June 11, 2002	Azegami et al.			
	6,405,299	June 11, 2002	Vorbach et al.			
	6,421,817	July 16, 2002	Mohan et al.			
)	6,425,068	July 23, 2002	Vorbach et al.			
M	6,457,116	September 24, 2002	Mirsky et al.			

EXAMINER'S INITIALS	PATENT/ PUBLICATION NUMBER	PATENT/ PUBLICATION DATE	NAME	CLAS S	SUB CLASS	FILING DATE
60	6,477,643	November 5, 2002	Vorbach et al.			
<u> </u>	6,480,937	November 12, 2002	Vorbach et al.			
	6,480,954	November 12, 2002	Trimberger et al.			
	6,496,971	December 17, 2002	Lesea et al.	1		
	6,513,077	January 28, 2003	Vorbach et al.			
	6,519,674	February 11, 2003	Lam et al.			
	6,526,520	February 25, 2003	Vorbach et al.			
	6,538,468	March 25, 2003	Moore			
	6,539,477	March 25, 2003	Seawright			ļ
	6,542,998	April 1, 2003	Vorbach et al.			
	6,571,381	May 27, 2003	Vorbach et al.			
	6,587,939	July 1, 2003	Takano			
	6,657,457	December 2, 2003	Hanrahan et al.	 		
	6,687,788	February 3, 2004	Vorbach et al.			
	6,697,979	February 24, 2003	Vorbach et al.	 		
	6,704,816	March 9, 2004	Burke			
	6,717,436	April 6, 2004	Kress et al.			
	6,728,871	April 27, 2004	Vorbach et al.	 		
	2002/0038414	March 28, 2002	Taylor et al.	1		
	2002/0045952	April 18, 2002	Blemel	1		_
	2002/0143505	October 3, 2002	Drusinsky		\	
	2002/0144229	October 3, 2002	Hanrahan		\	
	2002/0165886	November 7, 2002	Lam		+	
	2003/0123579	July 3, 2003	Safavi et al.		 	
	2003/0014743	January 16, 2003	Cooke et al.			
	2003/0046607	March 6, 2003	Vorbach			
	2003/0052711	March 20, 2003	Taylor et al.		1	
	2003/0055861	March 20, 2003	Lai et al.		 	
	2003/0056085	March 2, 2003	Vorbach			
	2003/0056091	March 20, 2003	Greenberg			
	2003/0056202	March 20, 2003	Vorbach			_
	2003/0093662	May 15, 2003	Vorbach et al.			
	2003/0097513	May 22, 2003	Vorbach et al.			
	2003/0135686	July 17, 2003	Vorbach et al.			-
	2004/0015899	January 22, 2004	May et al.		 	
	2004/0025005	February 5, 2004	Vorbach et al.		1	1
w	2004/0168099	August 26, 2004	Vorbach et al		 	

FOREIGN PATENT DOCUMENTS

XAMINER'S INITIALS	DOCUMENT NUMBER		DATE			SUB-CLASS		LATION
		1 0 000 457	3 24 1006				YES	NO
M	A B	0 208 457 0 221 360	June 24, 1986 May 13, 1987	Europe	\			
	C	0 428 327	May 22, 1991	Europe	\			
	D	0 463 721	January 2, 1992	Europe Europe	<u> </u>			-
	E	0 403 721	April 1, 1992					ļ
	F	0 477 809		Europe				<u> </u>
	L		May 20, 1992	Europe				
	G	0 497 029	August 5, 1992	Europe				<u> </u>
	H	0 539 595	May 5, 1993	Europe				
	I	0 628 917	December 14, 1994	Europe				
	J	0 678 985	October 25 1995	Europe				
	K	0 686 915	December 13, 1995	Europe				
	L	0 707 269	April 17 1996	Europe				
	М	0 726 532	July 2, 1998	Europe	/			
	N	0 735 685	October 2, 1996	Europe				<u> </u>
	0	0 748 051	December 11, 1996	Europe				
	P	0 835 685	October 2, 1996	Europe				1
-	Q	0 926 594	June 30, 1999	Europe		 		
	R	1 102 674	July 13, 1999	Europe		· · ·		
	S	1 146 432	October 17, 2001	Europe		 	l	
	T	2 752 466	February 20, 1998	France			<u> </u>	1
	U	38 55 673	November 20, 1996	Germany				
	V	42 21 278	January 5, 1994	Germany				
	W	44 16 881	November 17, 1994	Germany				
	Х	100 28 397	December 20, 2001	Germany		·		
	Y	100 36 627	February 14, 2002	Germany				-
	Z	101 29 237	April 18, 2002	Germany				·
	AA	102 04 044	August 14, 2003	Germany				
1	AB	196 51 075	June 10, 1998	Germany				
 	AC	196 54 593	July 2, 1998	Germany				-
-	AD	196 54 595	July 2, 1998	Germany				
	AE	196 54 846	July 9, 1998	Germany		 		
-	AF	197 04 044	August 13, 1998	Germany				
	AG	197 04 728	August 13, 1998	Germany		\ -		
	AH	197 04 742	September 24,	Germany				
	AI	198 07 872	August 26, 1999	Germany		 		
	AG	198 61 088	February 10, 2000	Germany		-		+
\sim	L	<u> </u>			<u> </u>	1		

·								
EXAMINER'S INITIALS	I	OOCUMENT NUMBER	DATE	COUNTRY	CLASS	CLASS SUB-CLASS		LATION
					1 -		YES	NO
/10-	AĶ	199 26 538	December 14, 2000	Germany				
- 19	AL	WO90/04835	May 3, 1990	PCT				
	AM	WO90/11648	October 4, 1990	PCT	1			
	AN	WO93/11503	June 10, 1993	PCT				
	AO	WO94/08399	April 14, 1994	PCT				
	AP	WO94/06077	March 17, 1994	PCT				
	AQ	WO95/00161	January 5, 1995	. PCT				
	AR	WO95/26001	September 28, 1995	PCT				
	AS	WO98/26356	June 18, 1998	PCT				
	AT	WO98/28697	July 2, 1998	PCT				
	AU	WO98/29952	July 9, 1998	PCT				
	AV	WO98/31102	July 16, 1998	PCT				
	AW	WO98/35299	August 13, 1998	PCT		\		
	AX	WO99/00731	January 7, 1999	PCT	<u></u>	 		
	AY	WO99/00739	January 7, 1999	PCT				
	AZ	WO99/32975	July 1, 1999	PCT				
	BA	WO99/40522	August 12, 1999	PCT				
	BB	WO99/44147	September 2, 1999	PCT				
.	BC	WO99/44120	September 2, 1999	PCT		·\		
.	BD	WO00/17771	March 30, 2000	PCT				
	BE	WO00/77652	December 21, 2000	PCT				
	BF	WO00/38087	June 29, 2000	PCT				
	BG	WO02/13000	February 14, 2002	PCT		\		
	ВН	WO02/21010	March 14, 2002	PCT		\		
	BI	WO02/29600	April 11, 2002	PCT		1		
	BJ	WO02/71248	September 12, 2002	PCT		\		
	BK	WO02/71249	September 12, 2002	PCT				
	BL	WO02/103532	December 27, 2002	PCT		\		
	BM	WO03/17095	February 27, 2003	PCT		\		
	BN	WO03/23616	March 30, 2003	PCT		 		
	BO	WO03/25781	March 27, 2003	PCT				
	BP	WO03/32975	April 24, 2003	PCT				
W	BQ	WO03/36507	May 1, 2003	PCT		 		
		<u> </u>				/		<u> </u>

OTHER DOCUMENTS

AMINER'S NITIALS		AUTHOR, TITLE, DATE, PERTINENT PAGES, ETC.
he	1	Ade et al., "Minimum Memory Buffers in DSP Applications," Electronics Letters, vol. 30, No. 6, March 17, 1994, pp. 469-471
T	2	Alippi, C., et al., "Determining the Optimum Extended Instruction Set Architecture for Application Specific Reconfigurable VLIW CPUs, IEEE., 2001, pp. 50-56
	3	Arabi et al., "PLD Integrates Dedicated High-speed Data Buffering, Complex State Machine, and Fast Decode Array," conference record on WESCON '93, Sep. 28, 1993, pp. 432-436
K	4	Athanas P. "A Functional Reconfigurable Architechture and Compiler for Adoptive Computing,", IEEE, pages 49-55:
	5	Athanas, P. et al., "An Adaptive Hardware Machine Architecture and Compiler for Dynamic Processor Reconfiguration" IEEE, Laboratory for Engineering Man/Machine Systems Division of Engineering, Box D, Brown University Providence, Rhode Island, 1991, pages 397-400
	6	Athanas, P. et al., "Quantitative analysis of floating point arithmetic on FPGA based custom computing machines," IEEE Symposium on FPGAs For Custom Computing Machines, IEEE Computer Society Press, April 19-21, 1995, pp. vii, 1-222
	7	Baumgarte, V., et al., PACT XPP "A Self-reconfigurable Data Processing Architecture," PACT Info. GMBH, Munchen Germany 2001
	8	Becker, J. et al., "Parallelization in Co-compilation for Configurable Accelerators - a Host/accelerator Partitioning Compilation Method," proceedings of Asia and South Pacific Design Automation Conference, Yokohama, Japan, February 10-13, 1998
	9	Bittner, R. A. Jr., "Wormhole Run-time Reconfiguration: Conceptualization and VLSI Design of a High Performance Computing System," <u>Dissertation</u> , January 23, 1997, pp. I-XX, 1-415
	10	Cadambi et al., "Managing Pipeline-reconfigurable FPGAs," ACM, 1998, pp. 55-64
	11	Callahan, T. et al. "The Garp Architerchture and C Copiler," Computer, April 2000, pages 62-69.
	12	Cardoso, J.M.P., "Compilation of Java TM Algorithms onto Reconfigurable Computing Systems with Exploitation of Operation-Level Parallelism," Ph.D. Thesis, Universidade Tecnica de Lisboa (UTL), Lisbon, Portugal October 2000 (English Abstract included)
	13	Diniz, P. et al., "Automatic Synthesis of Data Storage and Control Structures for FPGA-based Computing Engines", 2000, IEEE, pages 91-100
	14	Donandt, J. "Improving Response Time of Programmable Logic Controllers by Use of a Boolean Coprocessor", AEG Research Institute Berlin, IEEE, 1989, pages 4-167 - 4-169.
	15	Dutt, N. et al., "If Software is King for Systems-on-Silicon, What's New in Compiler?, IEEE., 1997, pp. 322-325
	16	Ferrante J. et al., "The Program Dependence Graph and its Use in Optimization ACM Transactions on Programming Languages and Systems," July 1987, USA, [online] Bd. 9, Nr., 3, pages 319-349, XP002156651 ISSN: 0164-0935 ACM Digital Library
	17	Fineberg, S. et al., "Experimental Analysis of a Mixed-Mode Parallel Architecture Using Bitonic Sequence Sorting", Vol. 11. No. 3, March 1991, pages 239-251
	18	Fornaciari, W. Et al., System-level power evaluation metrics, 1997 Proceedings of the 2nd Annual IEEE International Conference on Innovative Systems in Silicon, New York, NY, October 1997, pp. 323-330.
	19	Forstner, P. "Wer Zuerst Kommt, Mahlt Zuerst!: Teil 3: Einsatzgebiete und Anwendungsbeispiele von FIFO-Speichern Elektronik, August 2000, pages 104-109
	20	Gokhale, M. B. et al., "Automatic Allocation of Arrays to Memories in FPGA processors with Multiple Memory Banks Field-Programmable Custom Computing Machines, 1999, IEEE, pages 63-67 (\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \
	21	Hammes, J. et al., "Cameron: High Level Language Compilation for Reconfigurable Systems," Department of Compute Science, Colorado State University, Conference on Parallel Architectures and Compilation Techniques, October 12-16, 1999
	22	Hauck "The Roles of FPGA's in Reprogrammable Systems," IEEE, April 1998, pp. 615-638
	23	Hauser, J.R. et al., "Garp: A MIPS Processor with a Reconfigurable Coprocessor", University of California, Berkeley, IEEE, 1997, pages 12-21
	24	Hedge, S.J., "3D WASP Devices for On-line Signal and Data Processing, 1994, International Conference on Wafer Sca Integration, pages 11-21
	25	Hong Yu Xu et al., "Parallel QR Factorization on a Block Data Flow Architecture" Conference Proceeding Article, March 1, 1992, pages 332-336 XPO10255276, PAGE 333, Abstract 2.2, 2.3, 2.4 - page 334
	26	Hwang, L. et al., "Min-cut Replication in Partitioned Networks" IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems, [online] Bd. 14, Nr. 1, January 1995, pages 96-106, XP00053228 USA ISSN: 0278-0070 IEEE Xplore

INER'S TALS		AUTHOR, TITLE, DATE, PERTINENT PAGES, ETC.
he	27	Iseli, C., et al. "A C++ Compiler for FPGA Custom Execution Units Synthesis," IEEE. 1995, pp. 173-179 (NO MUNT
	28	Isshiki, Tsuyoshi et al., "Bit-Serial Pipeline Synthesis for Multi-FPGA Systems with C++ Design Capture," 1996 IEEE, pp. 38-47 () 0
	29	Jacob, J. et al., "Memory Interfacing and Instruction Specification for Reconfigurable Processors", ACM 1999, pages 145-154 (No WONTK)
	30	Jantsch, Axel et al., "A Case Study on Hardware/software Partitioning," Royal Institute of Technology, Kista, Sweden, April 10, 1994 IEEE, pp. 111-118
	31	John, L. et al., "A Dynamically Reconfigurable Interconnect for Array Processors", Vol. 6, No. 1, March 1998, IEEE, pages 150-157
	32	Koch, A. et al, "Practical Experiences with the SPARXIL Co-Processor", 1998, IEEE, pages 394 - 398
	33	Kung, "Deadlock Avoidance for Systolic Communication", 1988 Conference Proceedings of 15 th Annual International Symposium on Computer Architecture, May 30, 1988, pp. 252-260
	34	Ling, X., "WASMII: An MPLD with Data-Driven Control on a Virtual Hardware," Journal of Supercomputing, Kluwer Acdemic Publishers, Dordrecht, Netherlands, 1995, pp.253-276.
	35	Mano, M. Morris "Digital Design," by Prentice Hall, Inc., Englewood Cliffs, New Jersey 07632, 1984, pp. 119-125, 154-161.
	36	Maxfield, C. "Logic that Mutates While-U-Wait" EDN (Bur. Ed) (USA), EDN (European Edition), 7 November 1996, Cahners Publishing, USA
	37	Miller, M. J. et al., "High-Speed FIFOs Contend with Widely Differing Data Rates: Dual-port RAM Buffer and Dual-pointer System Provide Rapid, High-density Data Storage and Reduce Overhead", Computer Design, September 1, 1985, pages 83-86.
	38	Mirsky, E. DeHon, "MATRIX: A Reconfigurable Computing Architecture with Configurable Instruction Distribution and Deployable Resources," Proceedings of the IEEE Symposium on FPGAs for Custom Computing Machines, 1996, PP. 157-1666
	39	Myers, G. "Advances in Computer Architecture," Wiley-Interscience Publication, 2nd ed., John Wiley & Sons, Inc. pp. 463-94, 1978.
	40	Milsson et al., "The Scalable Tree Protocol - A Cache Coherence Approaches for Large-Scale Multiprocessors" IEEE, pp. 498-506 December 1992
	41	Norman, R. S., "Hyperchip Business Summary, The Opportunity," January 31, 2000, pages 1-3.
<u> </u>	42	Piotrowski, A. "IEC-BUS, Die Funktionsweise des IEC-Bus und seine Anwendung in Geräten und Systemen", 1987, Franzis-Verlag GmbH, München, pp. 20-25 (NO NO)
	43	Saleeba. M, "A Self-Contained Dynamically Reconfigurable Processor Architecture," Sixteenth Australian Computer Science Conference, ASCS-16, QLD, Australia, February, 1993.
	44	Schmit, H. Et al., Hidden Markov Modeling and Fuzzy Controllers in FPGAs, FPGAs for Custom Computing Machines, 1995; Proceedings, IEEE Symposium on Napa Valley, CA, April 1995, pp. 214-221.
	45	Siemers C. "Rechenfabrik Ansaetze Fuer Extrem Parallele Prozessoren", Verlag Heinze Heise GmbH., Hannover, DE No. 15, July 16, 2001, pages 170-179
	46	Simunic, T. Et al., Source Code Optimization and Profiling of Energy Consumation in Embedded Systems, Proceedings of the 13th International Symposium on System Synthesis, September 2000, pp. 193-198.
	47	Tau, E. et al., "A First Generation DPGA Implementation," FPD'95, pp. 138-143 (NO MONTH), 19 \$15
$\overline{}$	48	Tenca, A. F et al., "A Variable Long-Precision Arithmetic Unit Design for Reconfigurable Coprocessor Architectures", University of California, Los Angeles, 1998, pages 216 - 225.
1	49	The XPP White Paper, Release 2.1, PACT – A Technical Perspective, March 27, 2002, pages 1-27.
	50	TMS320C54X DSP: CPU and Peripherals, Texas Instruments, 1996, pp. 6-26 to 6-46 (NO MONTH) N
	51	TMS320C54x DSP: Mnemonic Instruction Set, Texas Instruments, 1996, p. 4-64 (NO MONTH)
	52	Villasenor, J. et al., "Configurable Computing Solutions for Automatic Target Recognition," IEEE, 1996 pp. 70-79
$-\bot$	53	Villasenor, J. et al., "Configurable Computing," Scientific American, Vol. 276, No. 6, June 1997, pp. 66-71.
	54	Villasensor, J. et al., "Express Letters Video Communications Using Rapidly Reconfigurable Hardware," IEEE Transactions on Circuits and Systems for Video Technology, IEEE, Inc. NY, December 1995, pp. 565-567.
	55	Wada K. et al., "A Performance Evaluation of Tree-based Coherent Distributed Shared Memory" Proceedings of the Pacific RIM Conference on Communications, Comput and Signal Processing, Victoria, May 19-21 1993 Weinbardt M. "Ulberstwingsmethoden for strukturnsgarennichen and have "Piecestein for Deltan der
tral	56	Weinhardt, M. "Ubersetzingsmethoden für strukturprogrammierbare rechner," Dissertation for Doktors der Ingenieurwissenschaften der Universität Karlsruhe: July 1, 1997 [Weinhardt, M. "Compilation Methods for Structure-

EXAMINER'S INITIALS		AUTHOR, TITLE, DATE, PERTINENT PAGES, ETC.
4	57	Weinhardt, M. et al., "Pipeline Vectorization for Reconfigurable Systems", 1999, IEEE, pages 52-60
	58	Wittig et al., "OneChip: An FPGA Processor with Reconfigurable Logic" IEEE, 1996 pp. 126-135 No MONTH
	59	Wu et al., "A New Cache Directory Scheme", IEEE, pp 466-472, June 1996
Ý	60	XLINX, "Logic Cell Array Families: XC4000, XC4000A and XC4000H", product description, pages 2-7 to 2-15, Additional XC3000, XC31000 and XC3100A Data, pages 8-16 and 9-14 (0 CTO 6 CM, 1999)
•	61	Ye, Z.A. et al., "A Compiler for a Processor With A Reconfigurable Functional Unit," FPGA 2000 ACM/SIGNA International Symposium on Field Programmable Gate Arrays, Monterey, CA Feb. 9-11, 2000, pp. 95-100.
he	62	Zhang, N. Et al., Architectural Evaluation of Flexible Digital Signal Processing for Wireless Receivers, Signals, Systems and Computers, 2000; Conference Record of the Thirty-Fourth Asilomar Conference, Bd.1, 29 October 2000, pp. 78-83.
EXAMINI	ER.	DATE CONSIDERED
·		10N E 4/20/07
		nitial if citation considered, whether or not citation is in conformance with M.P.F.P. 609; draw line through

citation if not in conformance and not considered. Include copy of this form with next communication to applicant.